



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Li Docket No.: 2003 P 54557 US
Filed: February, 3 2004 Examiner: Gene M. Munson
Serial No.: 10/771,075 Art Unit: 2811
Title: Transistor with Doped Gate Dielectric

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08A, which may be considered material to the examination of the above-identified application. A copy of the PCT International Search Report is provided in regard to the corresponding PCT application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

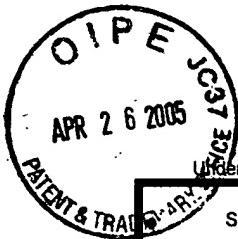
Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

Respectfully submitted,

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April 21, 2005

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APR 26 2005

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PTO/SB/08a (08-03)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/771,075
Filing Date	February 3, 2004
First Named Inventor	Li
Art Unit	2811
Examiner Name	Munson
Attorney Docket Number	2003 P 54557 US

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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